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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants :	Bernd Deutschmann, et al.	Art Unit :	Not Yet Assigned
Serial No. :	Not Yet Assigned	Examiner :	Not Yet Assigned
Filed :	Herewith	PCT Appln No.:	PCT/EP2005/001476
Title :	CIRCUIT ARRANGEMENT AND METHOD FOR PROTECTING AN INTEGRATED SEMICONDUCTOR CIRCUIT		

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PRELIMINARY AMENDMENT

Prior to examination, please amend the application as indicated on the following pages.

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